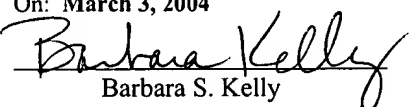


014-PAP

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

2661 #5 S.I. 03-11-04

| | | | |
|------------|---|---|---|
| Applicant | : | Brian Banister | I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service with sufficient postage as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450. On: March 3, 2004  Barbara S. Kelly |
| App. No. | : | 09/834,417 | |
| Filed | : | April 12, 2001 | |
| For | : | FAST FEEDBACK CHANNEL WITH FLEXIBLE BIT RELIABILITY FOR WIRELESS COMMUNICATIONS | |
| Group Art: | : | 2661 | |
| Examiner: | : | Unknown | |

RECEIVED

INFORMATION DISCLOSURE STATEMENT

MAR 09 2004

Commissioner for Patents
P.O. Box 1450
Washington, DC 22313-1450


Technology Center 2600

Pursuant to 37 C.F.R. §1.56 and in accordance with 37 C.F.R. §§1.97-1.98, information relating to the subject application is hereby disclosed. Inclusion of information in this statement is not to be construed as an admission that this information is material as that term is defined in 37 C.F.R. §1.56(b).

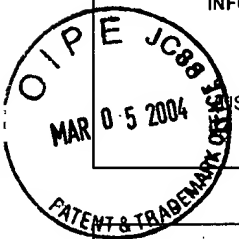
(X) No additional fee is due because this Information Disclosure Statement is being filed before the mailing date of a first Office Action on the merits of the above-identified application, in accordance with 37 CFR §1.97(b).

(XX) Copies of each of the references listed on the attached modified Form PTO-1449 are enclosed herewith. All of the listed references are in the English language.

Respectfully submitted,

Date: March 3, 2004
Signature **Erik M. Vieira**
Registration No. **53,723**

JAQUEZ & ASSOCIATES
750 B Street, Suite 2640
San Diego, CA 92101
(619) 238-1814 (voice)
(619) 238-2426 (fax)

[illegible]

RECEIVED

MAR 09 2004

Technology Center 2600

| EXAMINER INITIAL | OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.) |
|---------------------|---|
| | Andrew J. Viterbi, "An Intuitive Justification and a Simplified Implementation of the MAP Decoder for Convolutional Codes", IEEE Journal on Selected Areas in Communications, Vol. 16, No. 2, February 1998, pp. 260-264. |
| | Benedetto, et al. "Serial Concatenation of Interleaved Codes: Performance Analysis, Design, and Iterative Decoding", IEEE Transactions on Information Theory, Vol. 44, No. 3, May 1998, pp. 909-926. |
| | |
| | |
| | |

| | |
|--|-----------------|
| EXAMINER | DATE CONSIDERED |
| <p>*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.</p> | |